



LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

(AUTONOMOUS)

Accredited by NAAC & NBA (CSE, IT, ECE, EEE & ME)

Approved by AICTE, New Delhi and Affiliated to JNTUK, Kakinada

L.B.Reddy Nagar, Mylavaram-521230, Krishna Dist, Andhra Pradesh, India

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date:28-05-2022

Minutes of Board of Studies (BOS) Meeting

1. Review of action taken report on the minutes of BOS Meeting held on 18.12.2021:

- The members present were informed that Five-day training program for students on AWS-Cloud Computing has been Completed as per schedule and the students are told to initiate projects by using the knowledge gained.
- It was told that proposed International Conference on Advances in Electronics, Communication, Signal Processing and Artificial Intelligence has been postponed to AY 2022-23 due to the non-acceptance from the publishing agencies.
- The members were informed that One week Faculty development program on Advances in Signal Processing during February, 2022 was completed and faculty were exposed on graph signal processing and other advances in signal processing domain.
- To increase the industry interaction, department has initiated faculty training programme at industry through Efftronics Pvt Ltd for 3 months.
- The members were informed that 530 students have been registered for Microsoft upskilling program.

2. Information on academic performance of the students for the AY 2021-2022 and placements:

- It was mentioned that 91.87 % students have been graduated for the 2018-22 batch.
- The results of the odd semester were disclosed to the members. It was told that the students are performing well in the examinations.
- The placement statistics for 2018 Admitted batch were informed to the members present. It was told that during the Academic year 2021-22 good

number of placements were observed with respect to head count as well as number of placements and among the 36 companies visited, 10 companies are core companies.

- It was also mentioned that department is trying very hard to increase the placements in core companies and MoU with Unistring Tech Solutions has been signed to move one step ahead in that direction.
- The members were informed that the average pay package stood at 4.41 lakhs per annum for the Academic year 2021-22.

3. Information on Teaching & Learning Process:

- It was informed that hybrid mode of teaching is being adopted in view of the non-availability of hours for syllabus coverage. In particular for the VI semester students the syllabus coverage became difficult due to the placement training.
- It was also told that even during the summer vacation, two weeks of online classes were taken for VI semester and one week online for IV semester so that academic calendar is not altered.
- Flipped classroom approach is being adopted slowly to make the students practice themselves upon introducing the basic concepts.
- As part of the internship activity, online internship is suggested for VI semester students and off line for IV semester students. The students who need to do online were told to register through internshala or any other reputed platforms.
- It was informed that offline activities under Professional bodies IEEE, ISTE, IETE activities are scheduled monthly to improve students' technical knowledge and communication skills.
- It was told that significant number of students have been registered for IETE and IEEE.
- The members were informed that department technical club is organizing events regularly to enhance the communication skills of the students.

4. Approval of MOOCs for Honor and Minor Programme:

- It was told that for getting honor degree, student need to two courses under MOOCs along with four regular courses.

- It was mentioned that VLSI and Embedded System as one course under 12 weeks NPTEL course along with VLSI verification and physical design as the department is possessing the necessary hardware and software.
- Dr.N.V.S.N.Sarma told that instead of giving single course it is better to give choice to students for selecting a specific course under MOOCs.
- The courses under minor program are also discussed.

5. Information and discussion on rubrics for identifying the best academic project:

- Various rubrics for identifying the best project were disclosed to the members. It was also told that for best project among all the three sections, Rs.5000 is being given as the cash prize.
- Dr.P.Srihari Rao told that incorporation of Modern tools usage is not opt as it depends on the availability of tools.


6. Discussion and approval of syllabi of B. Tech V to VIII Semesters, COs and CO-PO articulation matrix:

- The detailed syllabus for the B. Tech V to VIII Semesters was discussed along with Course outcomes and articulation matrices.
- Dr.N.V.S.N.Sarma has told to rename the design and simulation of antenna structures as design and simulation of antennas.
- Dr.P.Srihari Rao has told to rename the Digital CMOSIC design as Digital IC design.
- For Digital Communications Course , Dr.P.Srihari Rao has told to shift pulse digital modulation techniques to fundamental course in communication course so that Digital communications contents are more effectively delivered.
- Further, it was told by Dr.P.Srihari Rao told to move the Digital communication model along with orthogonalization procedure to Unit-I before introducing the Digital Modulation Techniques and it is better to follow the flow given in relevant text book by Simon Haykin.
- In Antennas and wave propagation, it was told to emphasize more on space wave propagation than sky wave propagation.
- In Computer networks, it opt to introduce one or two topics on network management as suggested by Dr.N.V.S.N.Sarma.
- In the skill-oriented course, Design and simulation of Antennas, it is better not to use Balanis as suggested by Dr.N.V.S.N.Sarma .

- In VLSI design, Unit I and II can be clubbed together as suggested by Dr.P.Srihari Rao and second chapter should focus on design building blocks. Sequential system design can be shifted to Digital IC Design and one complete chapter can be allocated for Analog IC Design.
- For VLSI Design lab, it is better to provide specifications like propagation delay or power dissipation and then design the systems as suggested by Dr.P.Srihari Rao.
- In Microwave engineering lab, scattering parameters for all types of ports can be placed together and experiments 3 and 4 can be interchanged and for designing experiments evaluation can be added as suggested by Dr.N.V.S.N.Sarma .

7. Information on events organized for the A.Y 2021-22 and calendar of events for the A.Y. 2022-23:

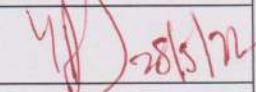
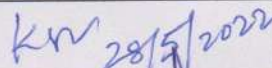
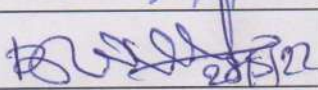
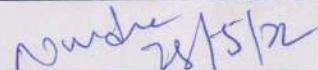
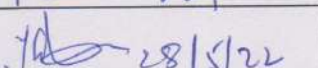
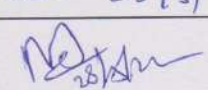
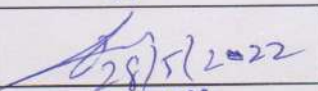
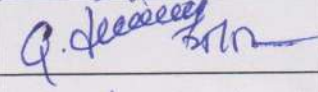
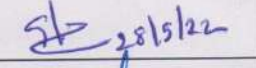
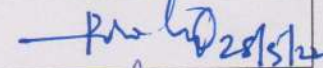
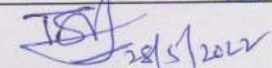
- The events that have been organized during A.Y:2021-22 were informed to the members present.
- Various events scheduled for the Academic year 2022-23 were also discussed in detail. It was mentioned that along with faculty, technical staff are also going to be sent to external training programs.
- Dr.N.V.S.N.Sarma told that it is better to have alumni interaction programs as part of calendar of events where the existing students can gain expertise in managerial skills due to the interaction with Alumni.


Chairman

Board of Studies

HEAD

Department of Electronics &
Communication Engineering
Lakireddy Bali Reddy College of Engineering
MYLAVARAM, Krishna Dt., Andhra Pradesh

S.No	Name & Designation	Position in BOS	Signature with Date
1.	Dr.Y.Amar Babu, Professor & HOD,	Chairman	 28/5/22
2.	Dr.U.V.Ratna Kumari Professor of ECE, University College of Engineering, JNTUK, Kakinada	University Nominee	Attended online
3.	Dr. N.V.S.N.Sarma Director, Indian Institute of Information Technology, Trichy	External Member, Subject Expert	Attended online
4.	Dr.P.Srihari Rao, Associate Professor , Department of ECE, NIT, Warangal	External Member, Subject Expert	Attended online
5.	Dr.John Bedford Solomon, Principal Engineer, Insemi Technology Limited, Secunderabad	External Member, Industry	Attended online
6.	Dr.B.Anil Kumar, Associate Professor, Department of ECE, Gokaraju Rangaraju Institute of Engineering & Technology (Autonomous), Hyderabad	Alumni	Attended online
7.	Dr.E.V.Krishna Rao Professor ,Dean of R &D	Internal Member	 28/5/2022
8.	Prof.B.Ramesh Reddy Professor & IQAC In Charge	Internal Member	 28/5/22
9.	Dr.A.Narendra Babu Professor	Internal Member	 28/5/22
10.	Dr.Y.S.V.Raman Professor	Internal Member	 28/5/22
11.	Dr.M.Venkata Sudhakar Professor	Internal Member	 28/5/22
12.	Dr.P.Lachi Reddy Professor	Internal Member	 28/5/2022
13.	Dr.G.Srinivasulu Professor	Internal Member	 28/5/22
14.	Dr. G.L.N.Murthy Professor	Internal Member	 28/5/22
15.	Dr.B.Poornaiah, Professor	Internal Member	 28/5/22
16.	Dr.T.Satyanarayana, Professor	Internal Member	 28/5/2022